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TOWNSEND and TOWNSEND and CREW LLP

By: John S. Bunn Jr.

PATENT
Attorney Docket No.: 018865-001010US
Client Reference No.: 17732-7919

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Brian Sze-Ki Mo

Application No.: 10/077,258

Filed: February 14, 2002

For: METHOD OF FORMING A
TRENCH TRANSISTOR HAVING A
SUPERIOR GATE DIELECTRIC

Examiner: Jimmy Nguyen

Art Unit: 2829

**INFORMATION DISCLOSURE
STATEMENT UNDER 37 CFR §1.97 and
§1.98**



Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner.

It is respectfully requested that the cited references be expressly considered during the prosecution of this application, be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

2002/00000001 201430 10077253

This IDS is being filed before the mailing date of the final Office Action or
Notice of Allowance.

Please charge the IDS fee of \$180 to Deposit Account No. 20-1430. Please
deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,



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